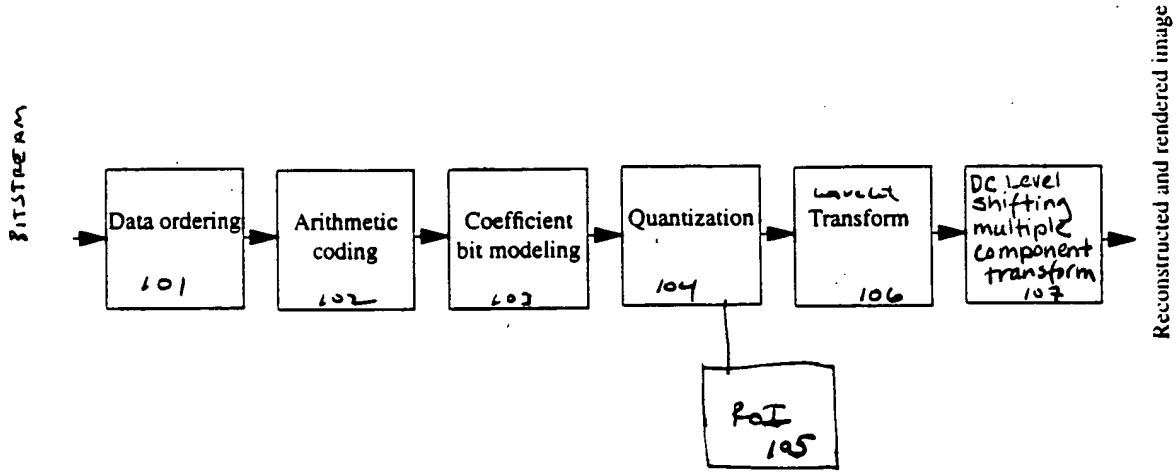

Figure 1

Document3 - 050205

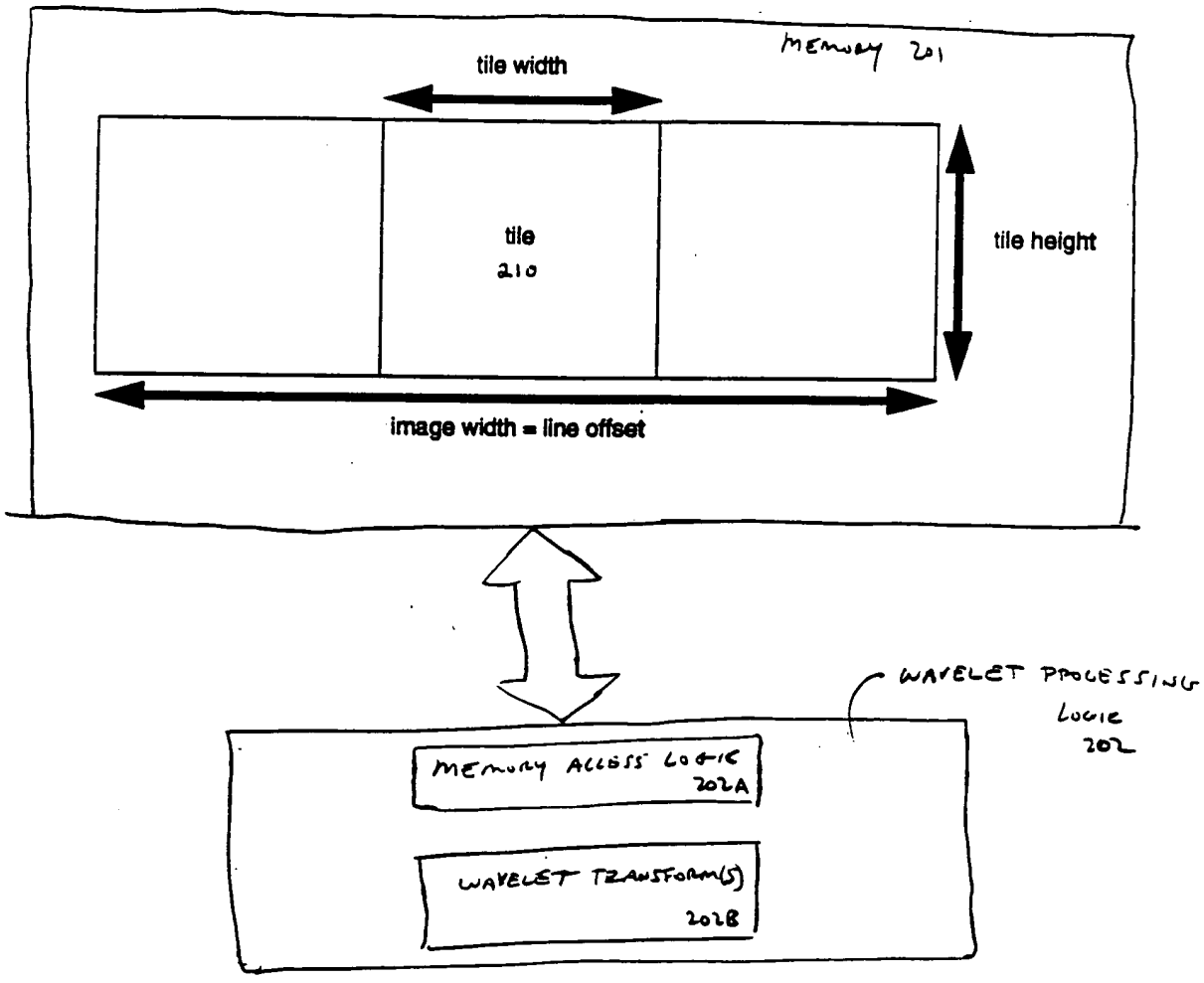
MEMORY 201
tile width
tile
210
tile height
image width = line offset
WAVELET PROCESSING LOGIC 202
MEMORY ACCESS LOGIC 202A
WAVELET TRANSFORM(S) 202B

Figure2

EP 0 982 938 A2

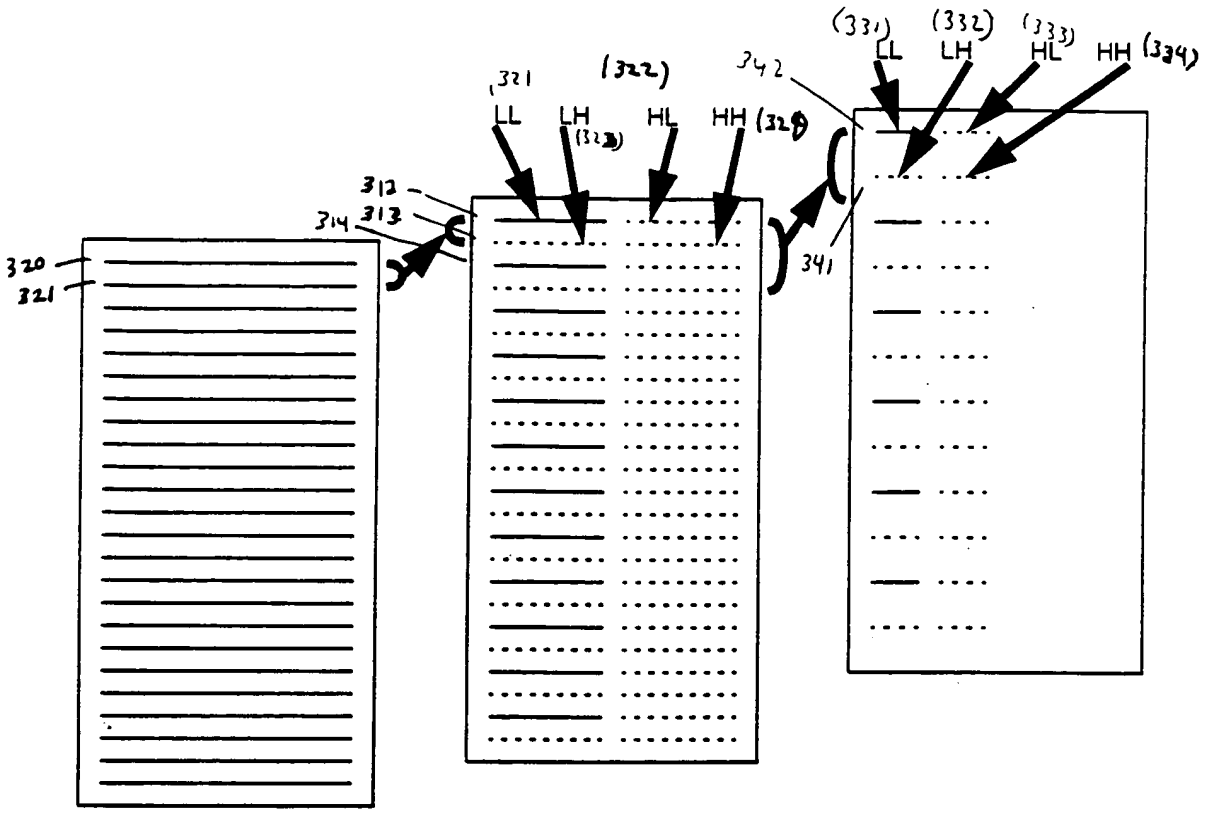

Figure 3

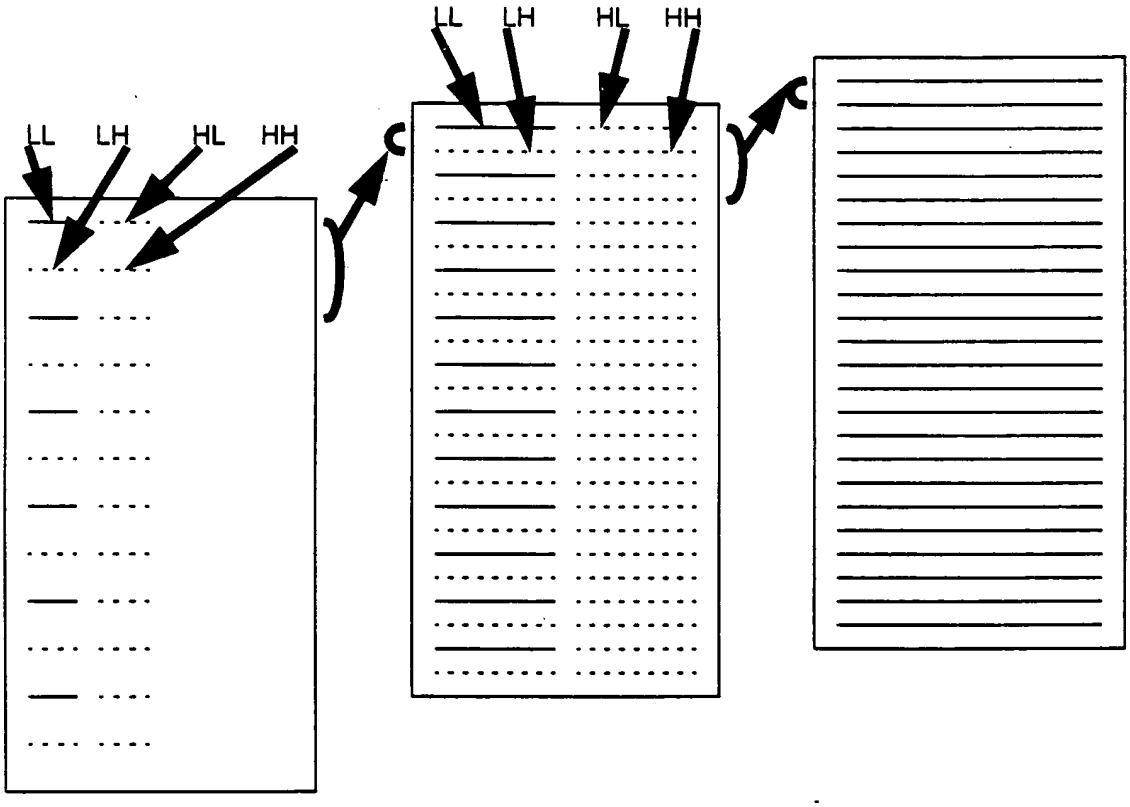

Figure 3

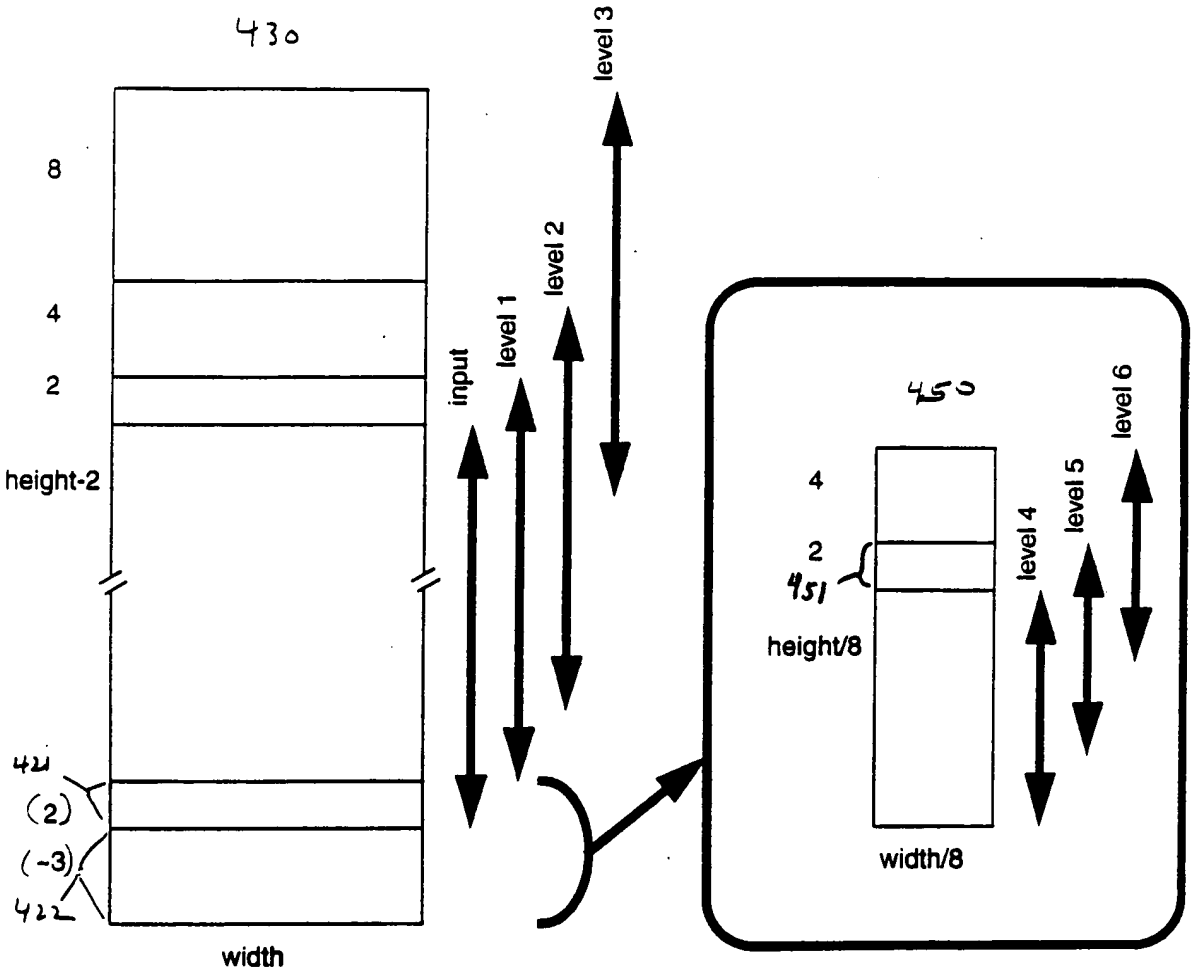
430
8
4
2
height-2
422
(2)
(-3)
422
width
input
level 1
level 2
level 3
450
4
2
451
height/8
width/8
level 4
level 5
level 6

Figure 4A

Docket: 2003-0203

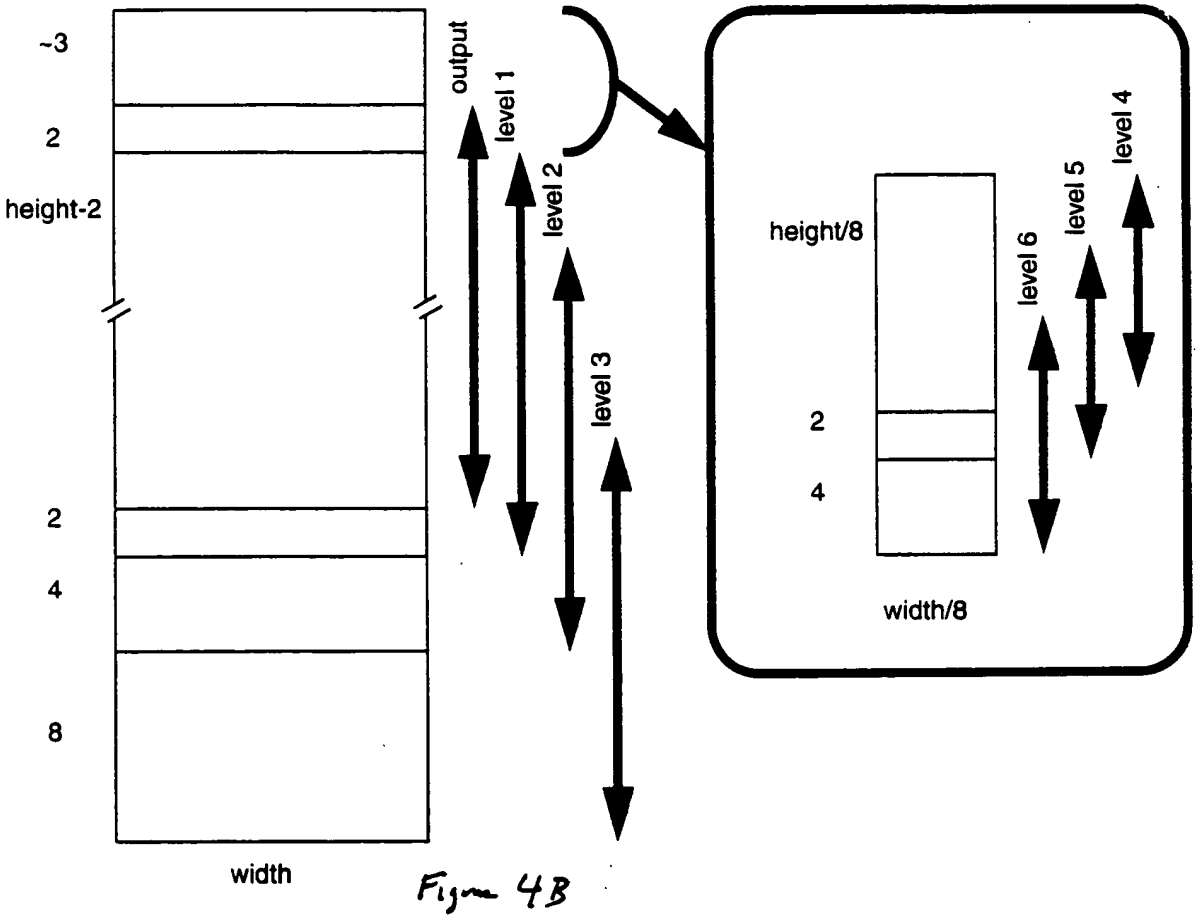

Figure 4B

Page 2 of 2

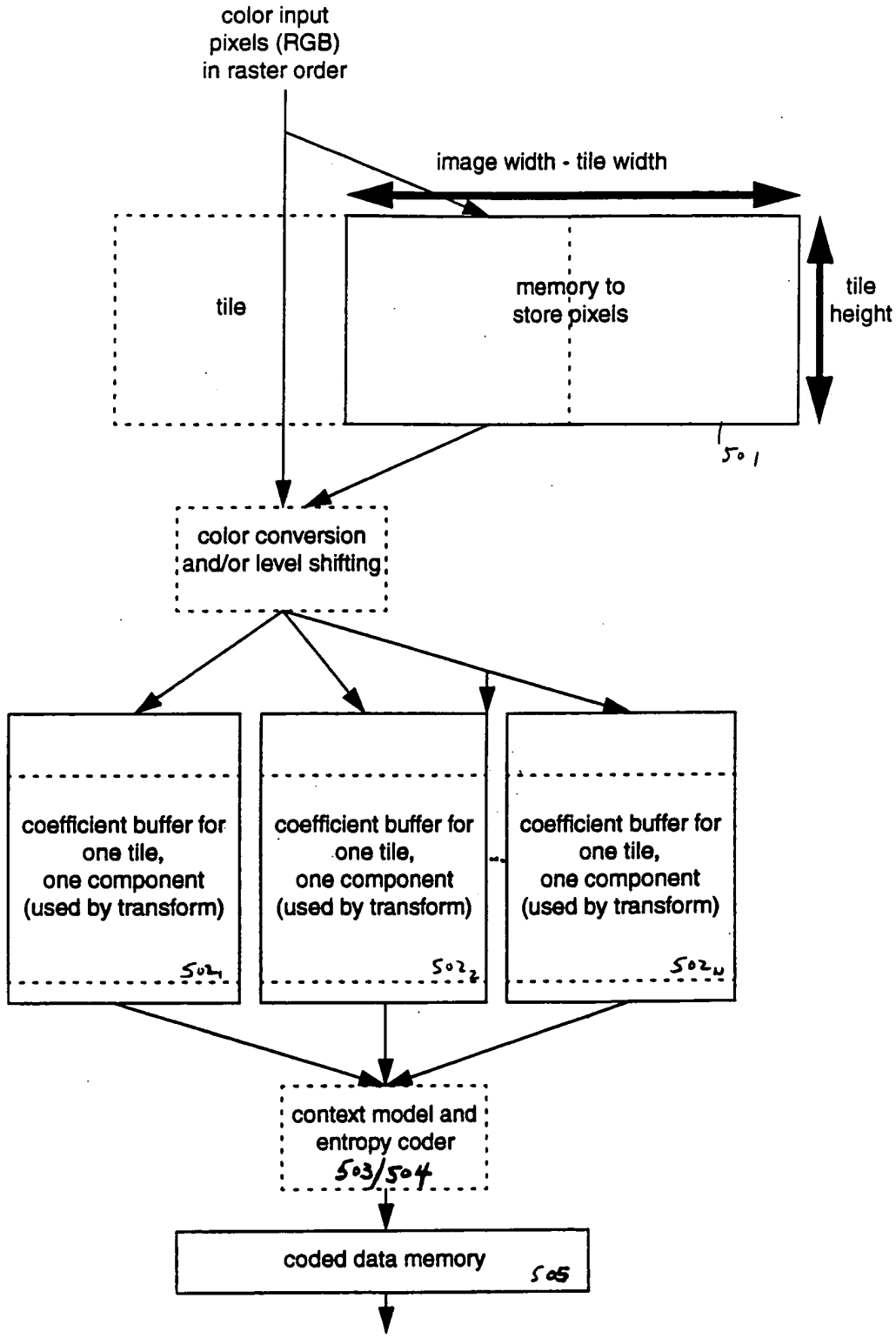

Figure 5

EXPRESS MAIL NO. EL 625 850 850

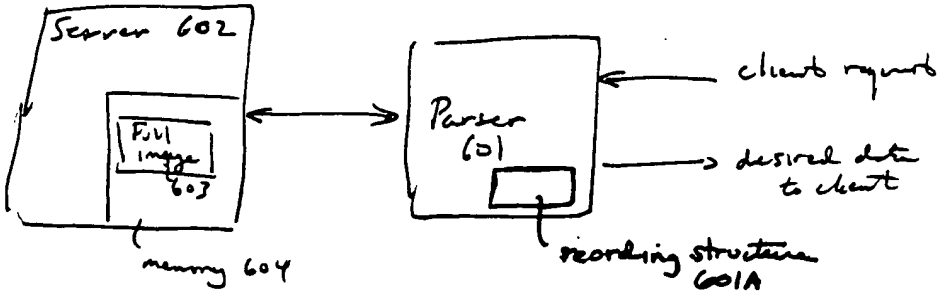

Figure 6A

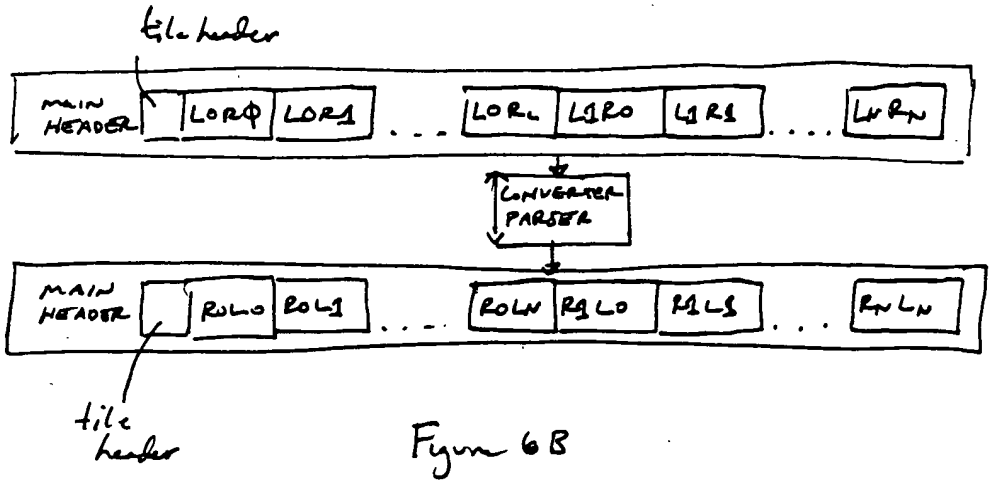

Figure 6B

09823-2003 · 2003-02-07

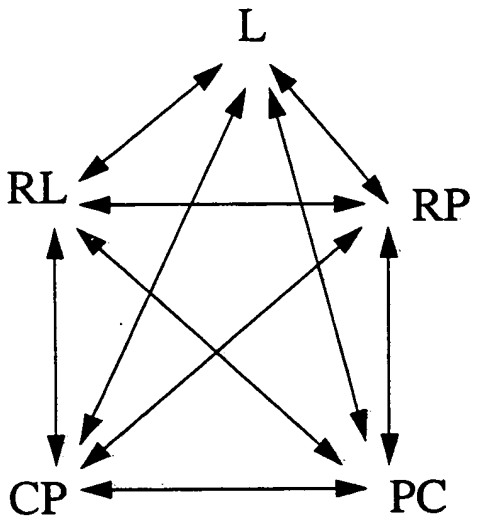

Figure 7A

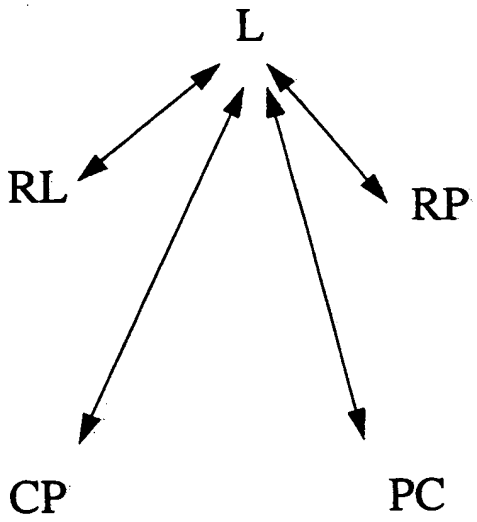

Figure 7B

7608023-080203

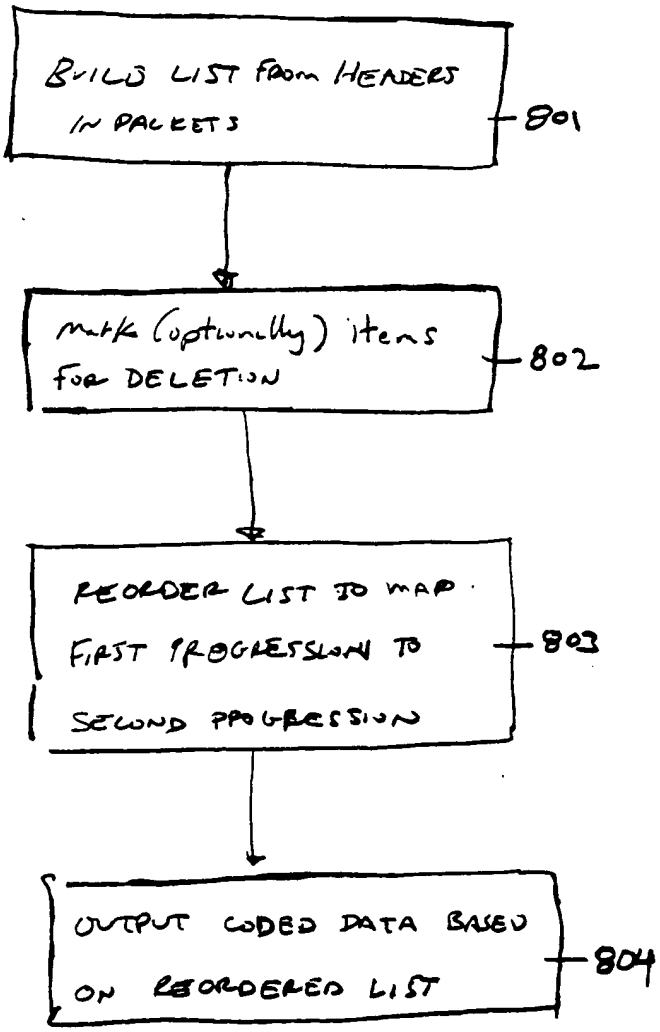

Figure 8

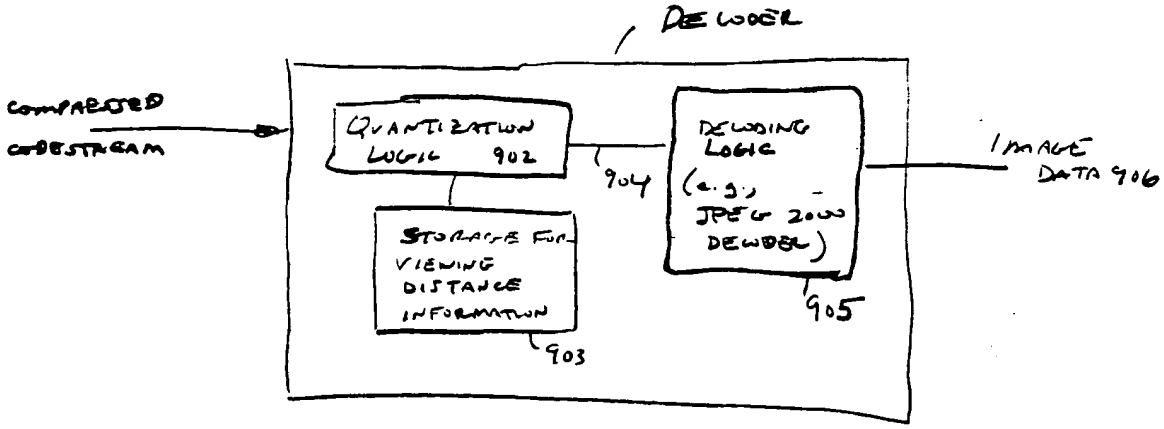

Figure 9

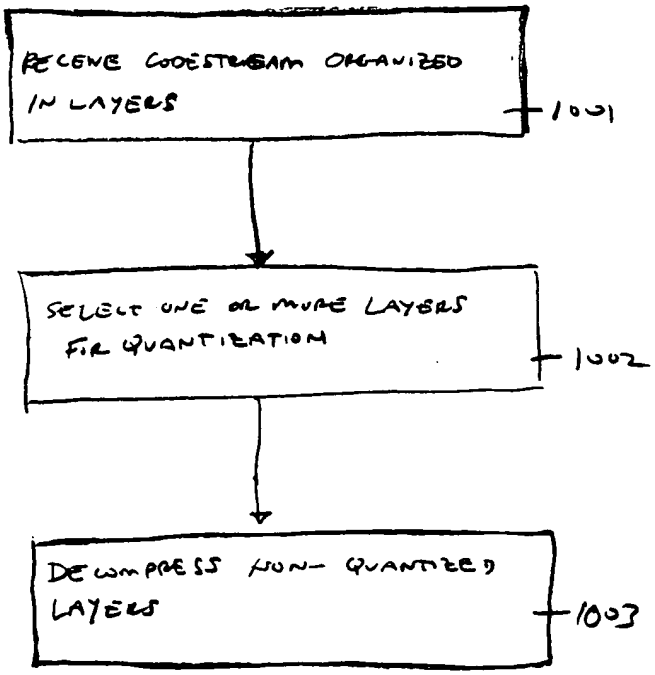

Figure 10

Document: 2005-0523

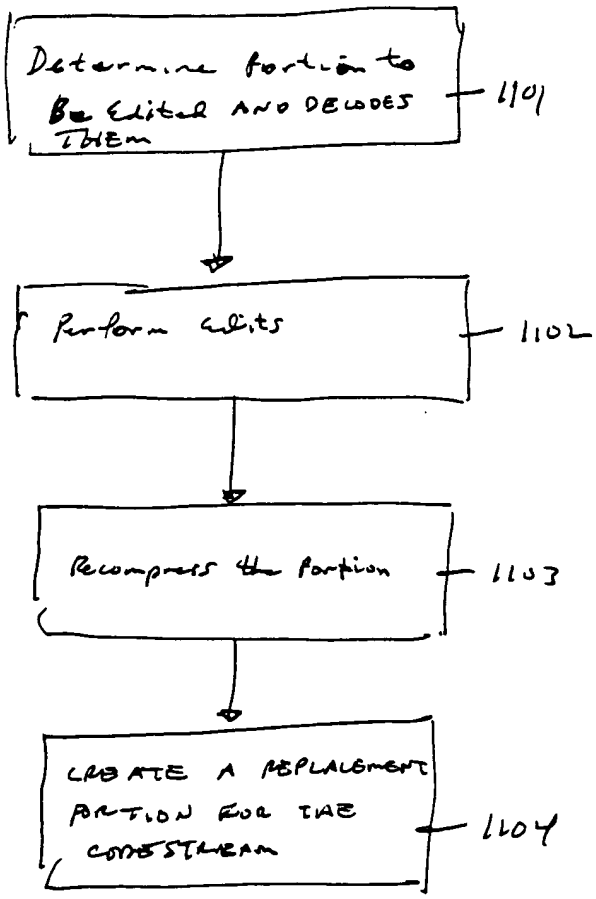
Determine portion to Be Edited AND DECODES THEM
1101
Perform edits
1102
Recompress the portion
1103
CREATE A REPLACEMENT PORTION FOR THE COMPRESSED STREAM
1104

Figure 11

Patent Application Publication

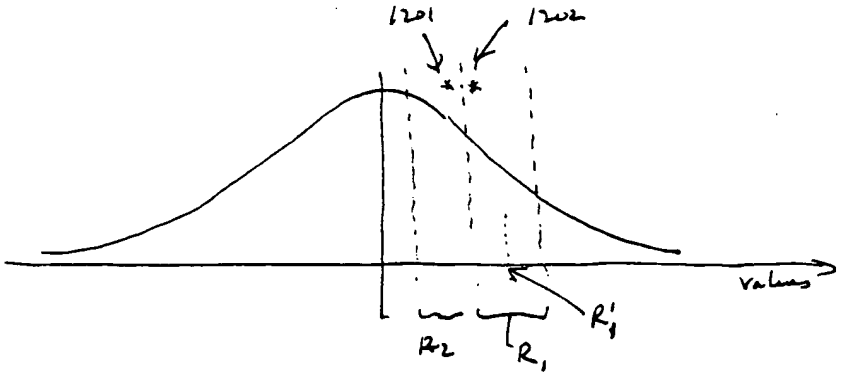

Figure 12

EXPRESS MAIL NO. EL 928 023 248 US

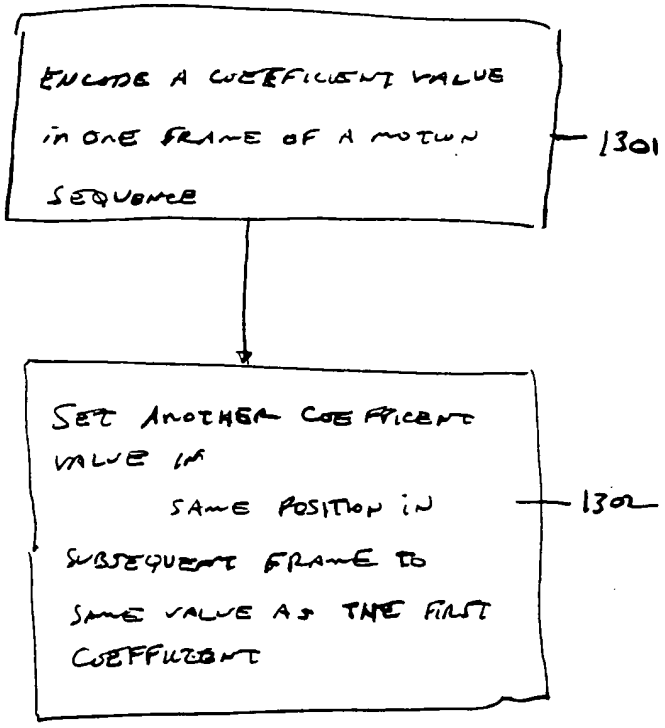

Figure 13

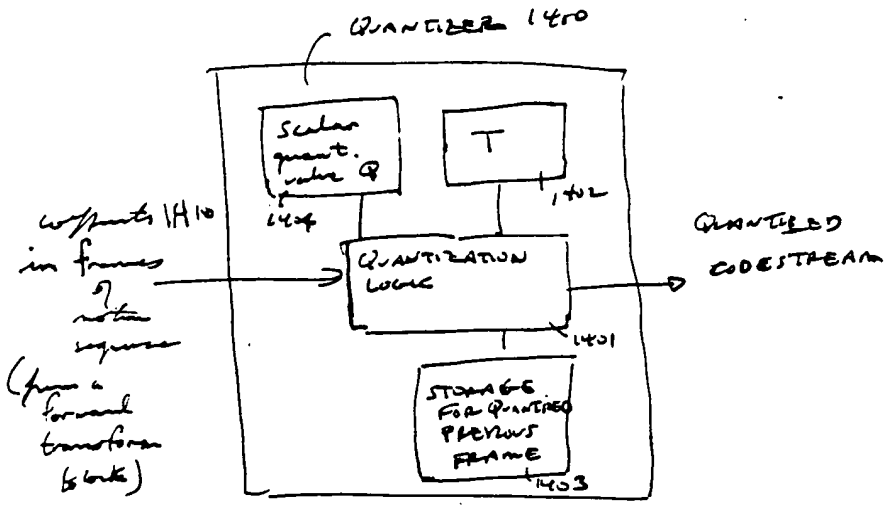

Figure 14

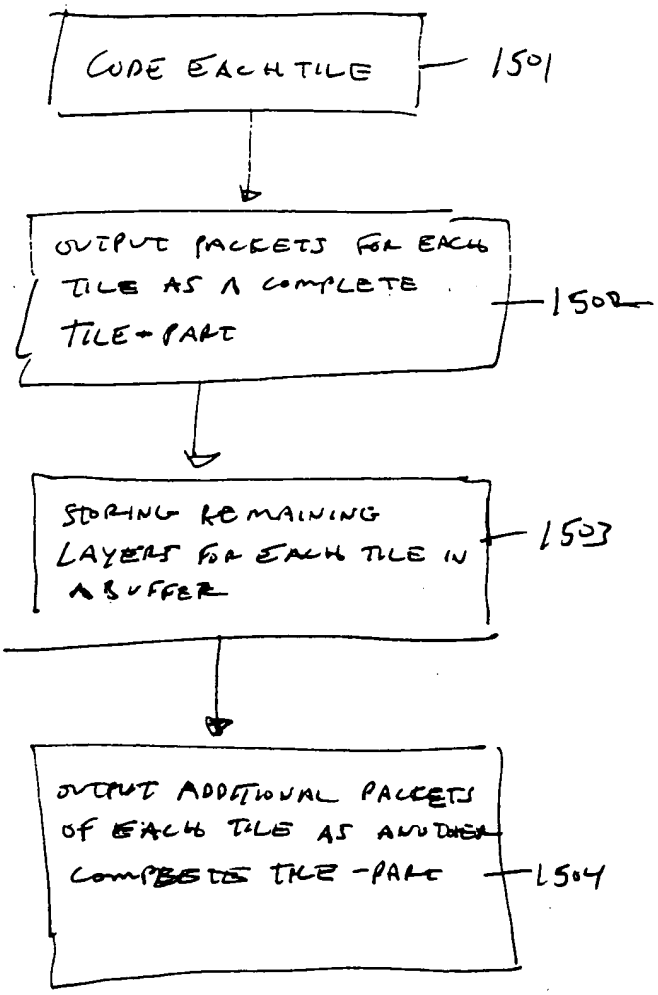
CODE EACH TILE
1501
OUTPUT PACKETS FOR EACH TILE AS A COMPLETE TILE-PART
1502
STORING REMAINING LAYERS FOR EACH TILE IN A BUFFER
1503
OUTPUT ADDITIONAL PACKETS OF EACH TILE AS ANOTHER COMPLETE TILE-PART
1504

Figure 15A

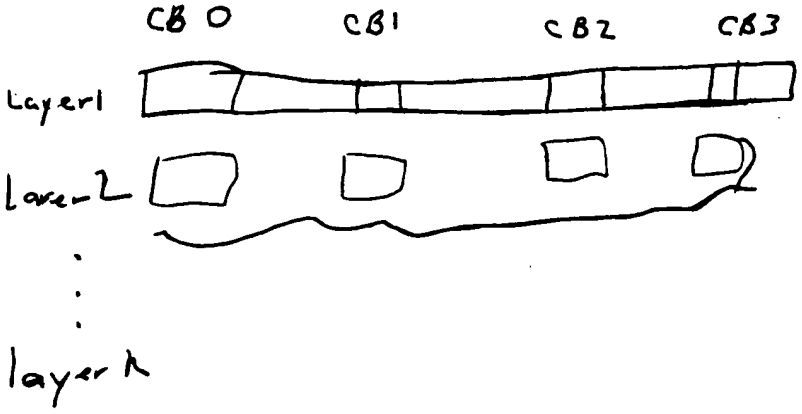

Fig 15B

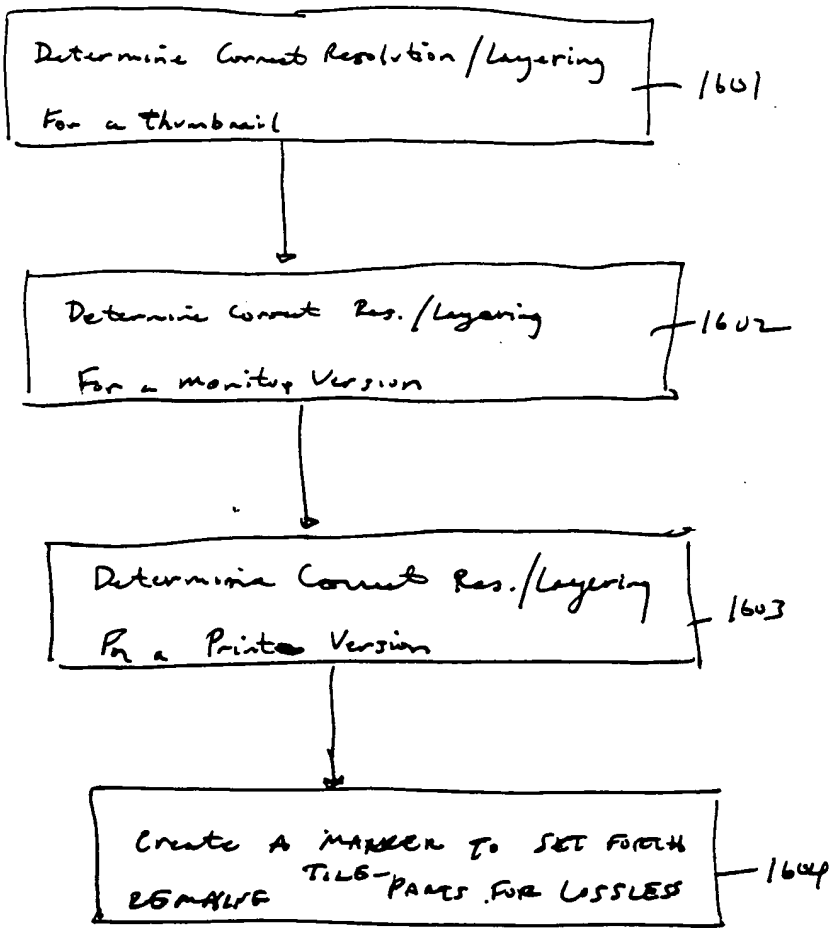
Determine Correct Resolution / Layering
For a thumbnail
1601
Determine Correct Res. / Layering
For a Monitor Version
1602
Determine Correct Res. / Layering
For a Printer Version
1603
Create a MARKER TO SET FORTH TILE-PARTS FOR LOSSLESS REGIONS
1604

Figure 16

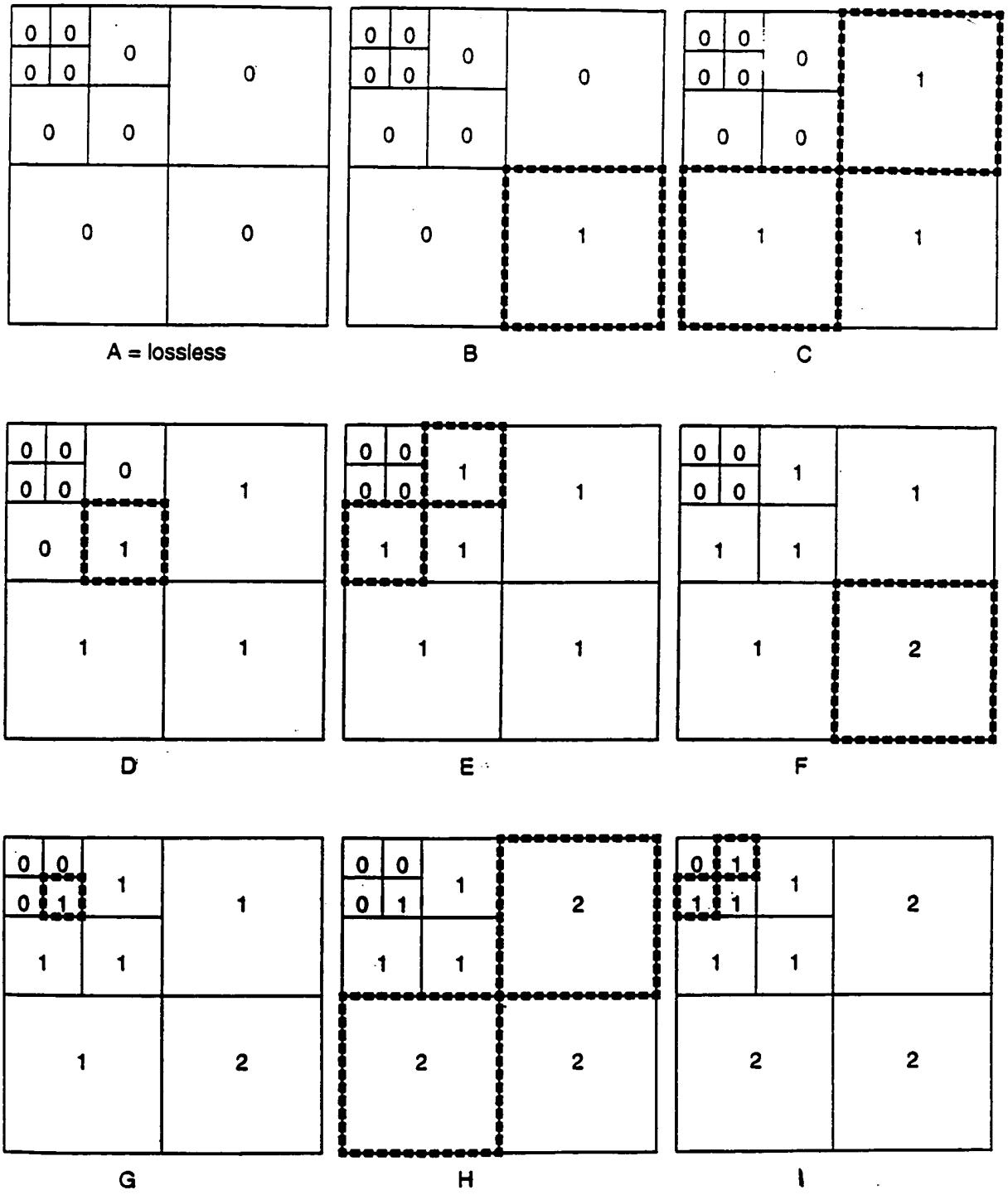

Figure 17

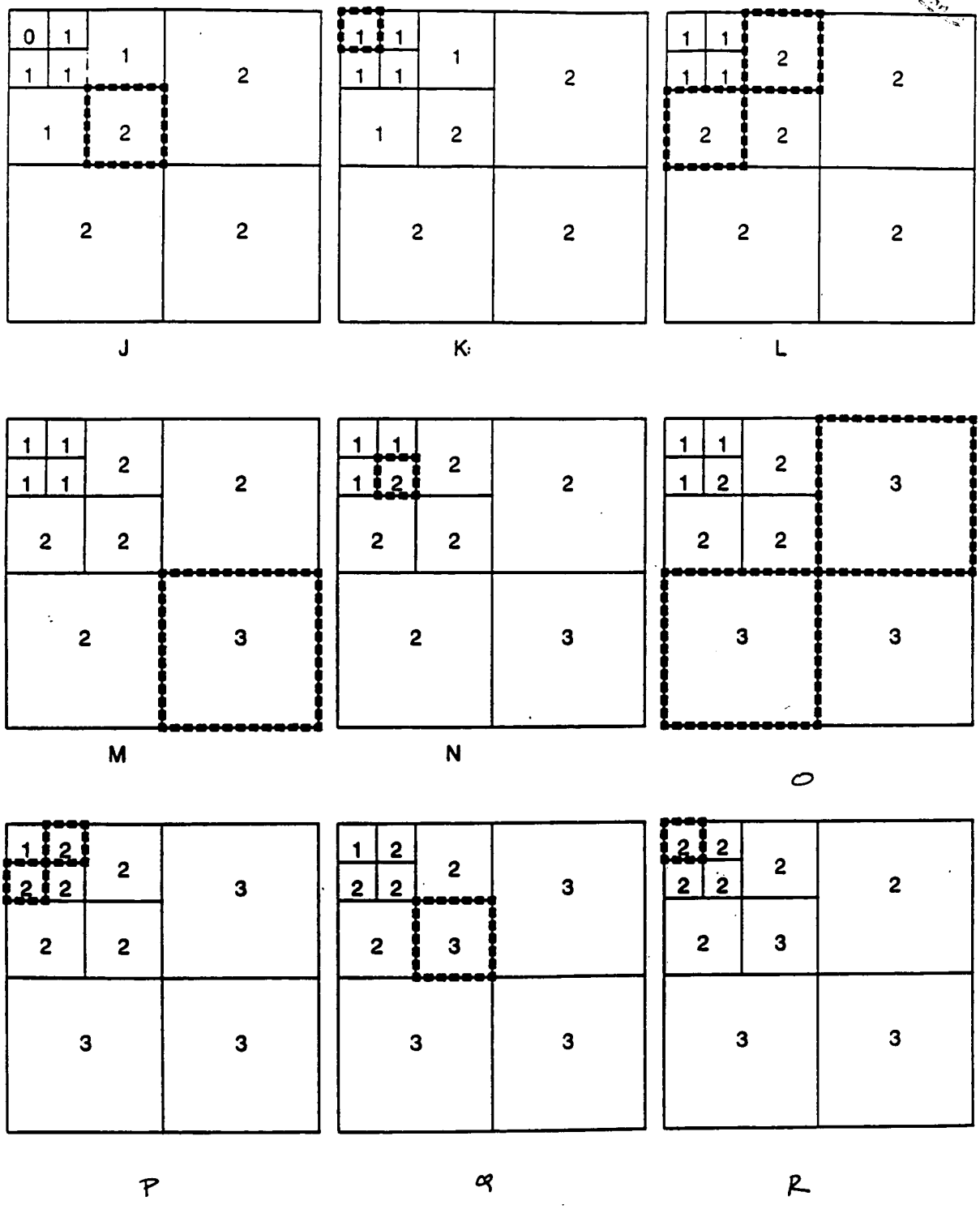

Figure 18

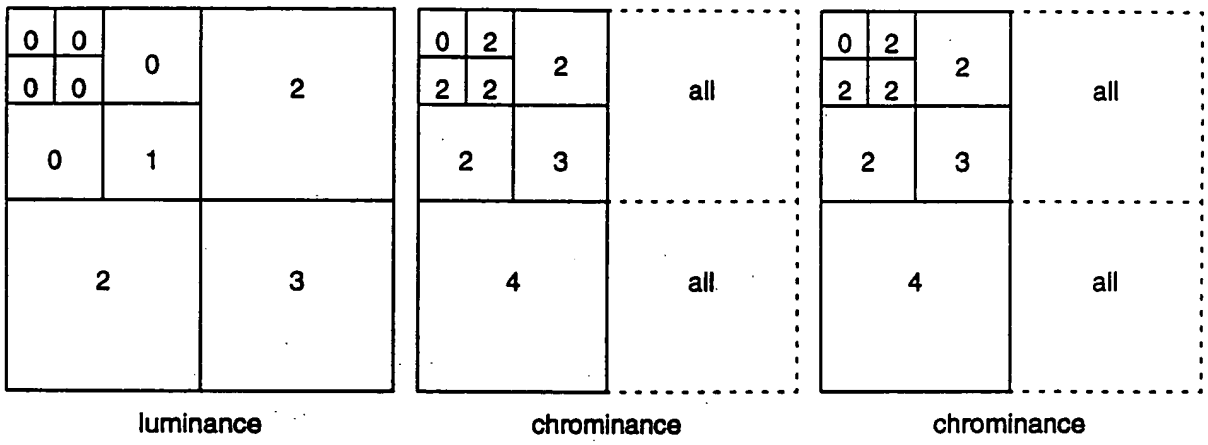

Figure 19

5,805,735 - 09/08/98

2000

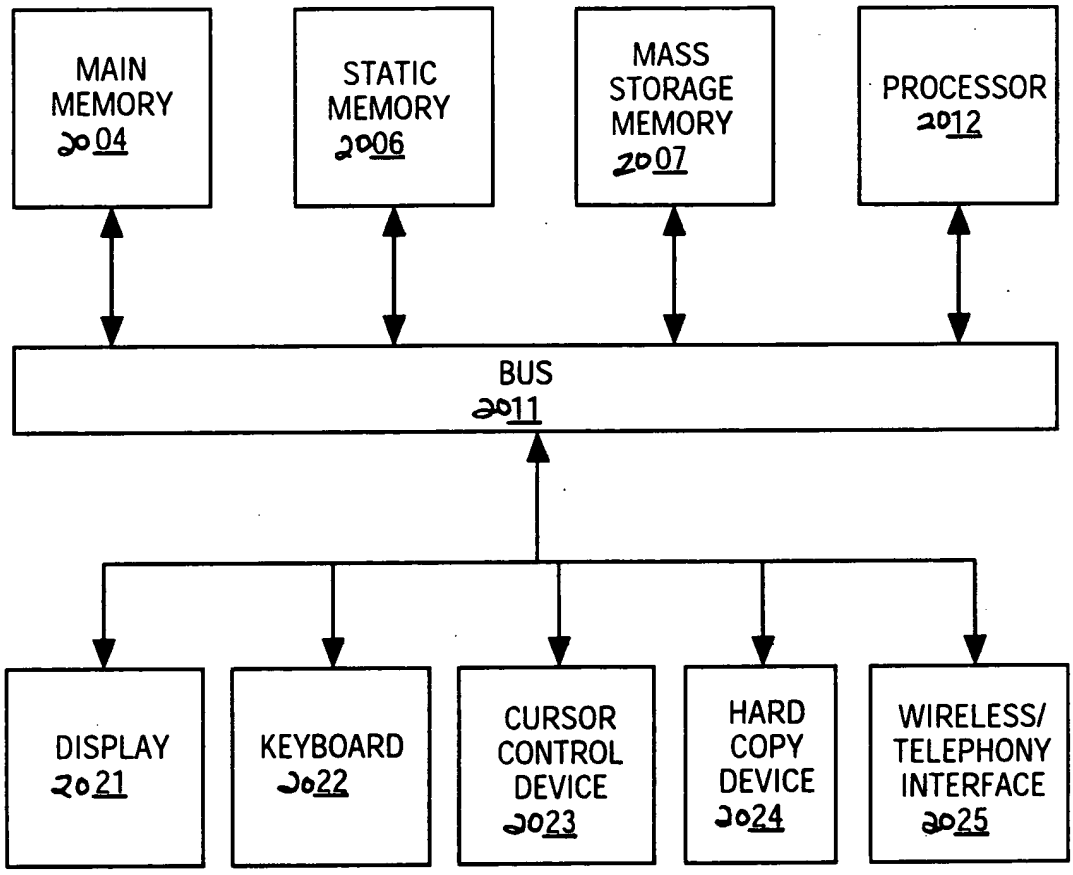
MAIN MEMORY 2004
STATIC MEMORY 2006
MASS STORAGE MEMORY 2007
PROCESSOR 2012
BUS 2011
DISPLAY 2021
KEYBOARD 2022
CURSOR CONTROL DEVICE 2023
HARD COPY DEVICE 2024
WIRELESS/ TELEPHONY INTERFACE 2025

FIG. 20

EP 0 903 943 A2

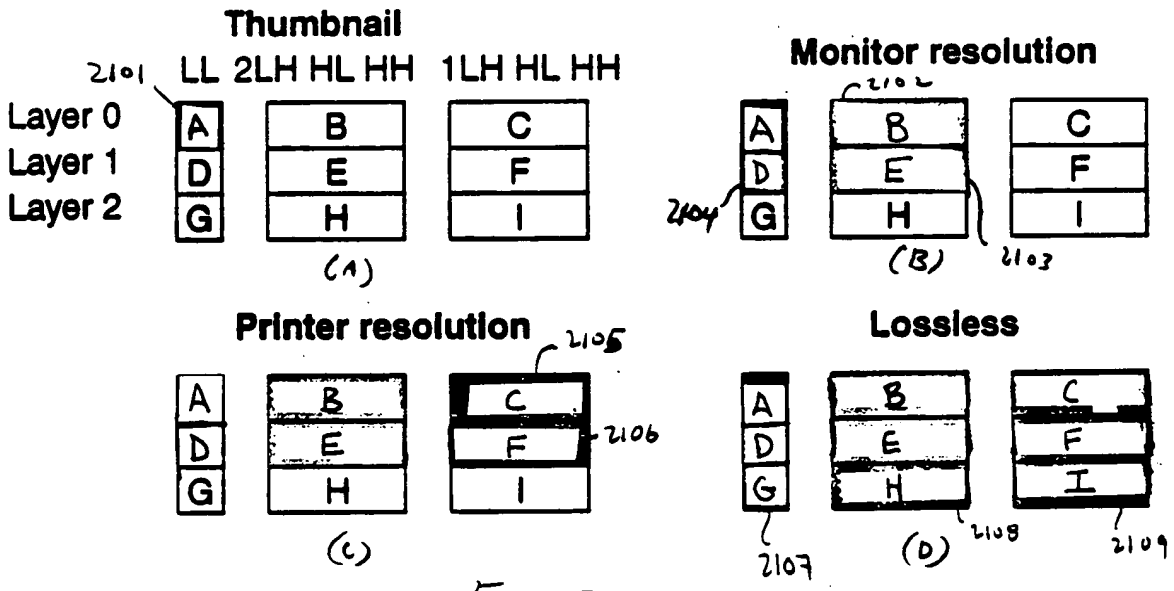

Figure 21

EXHIBIT 2004

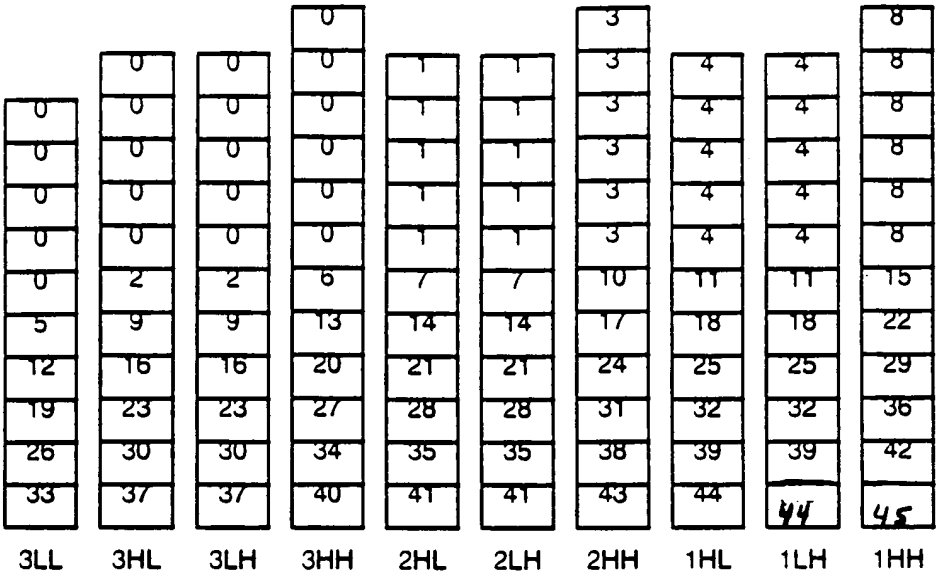

Figure 22

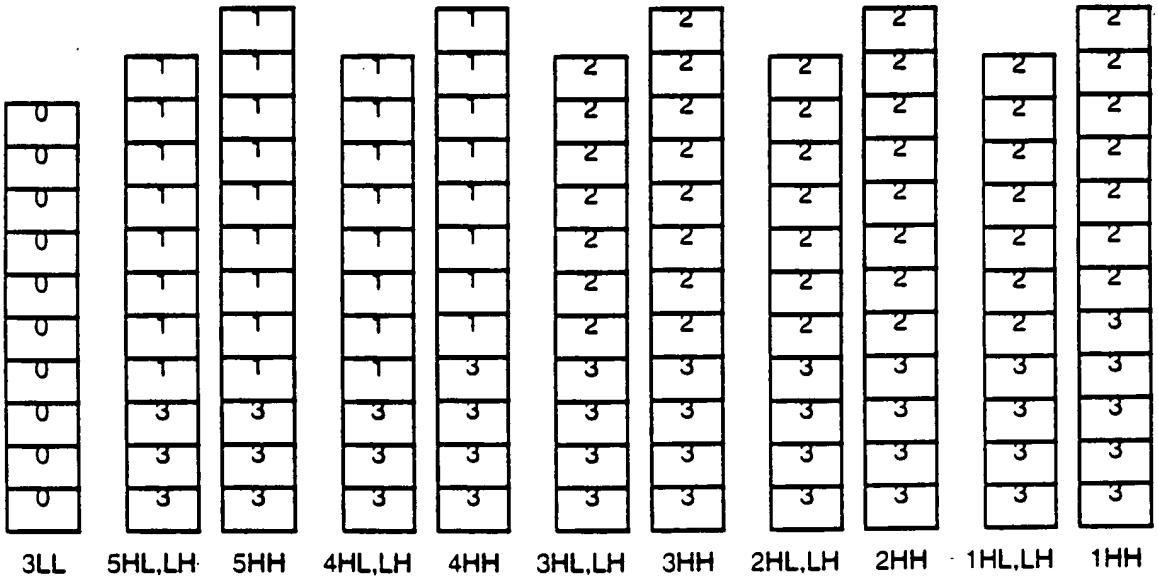

Figure 23

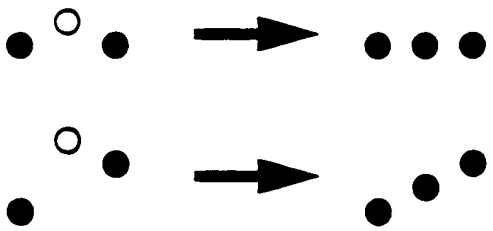

Fig. 24

05800634 - 08/20/04

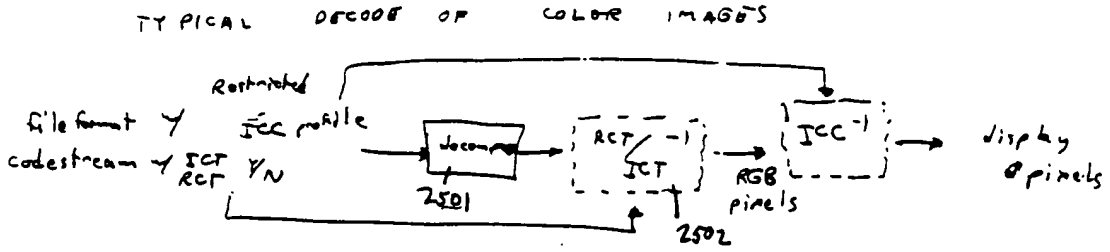

Figure 25

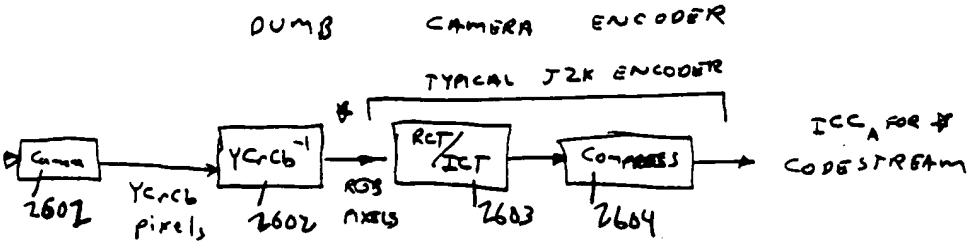

Figure 26

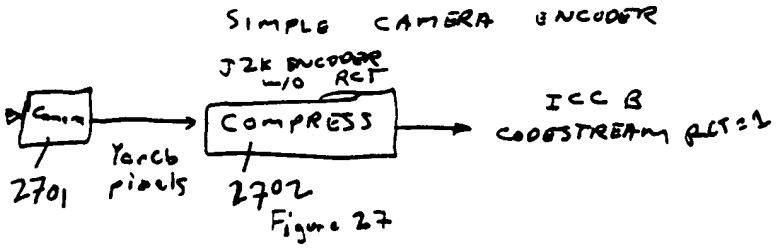

Figure 27

EP 0 928 109 A2

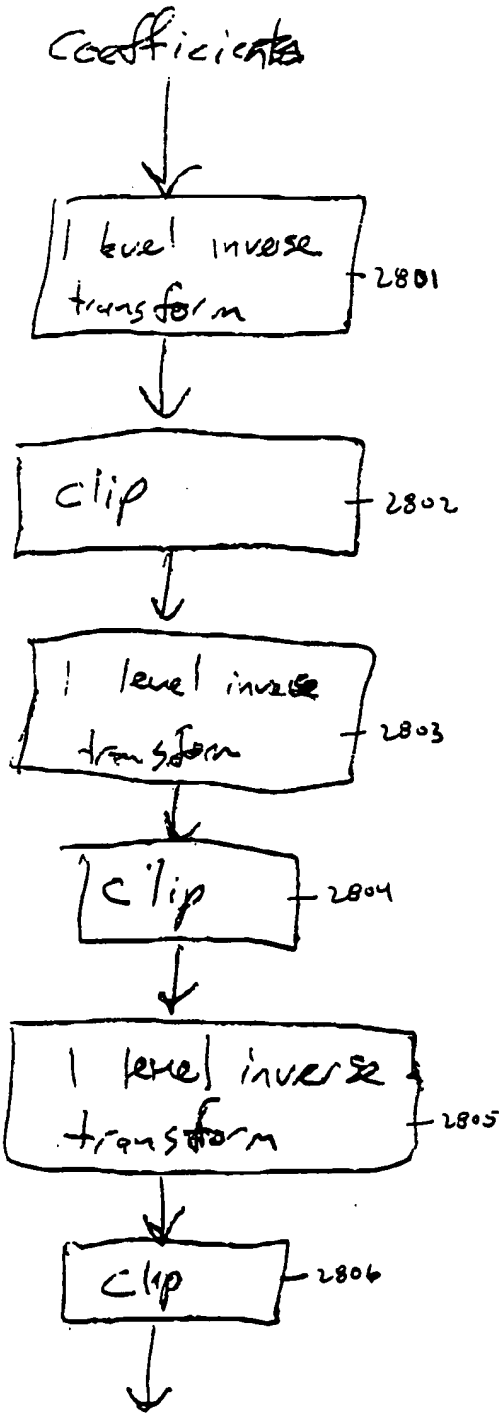

Figure 28